

**What is claimed is:**

**[Claim 1]** 1. A method of forming conductive column in a fabrication of a circuit board, the circuit board comprising a dielectric layer formed thereon, the method comprising:

forming a first blind hole in a first surface of the dielectric layer;  
forming a second blind hole in a second surface of the dielectric layer opposite to the first surface, a blind end of the first blind hole connecting to a blind end of the second blind hole, the first blind hole and the second blind hole constituting a through hole, wherein an inner diameter of the through hole near the first surface or the second surface is substantially larger than an inner diameter of the through hole near a middle portion of the through hole; and  
filling a conductive material in the through hole to form a conductive column.

**[Claim 2]** 2. The method of forming conductive column of claim 1, wherein the first blind hole and the second blind hole have a cone shape, and the through hole and the conductive column have an hourglass shape.

**[Claim 3]** 3. The method of forming conductive column of claim 1, wherein the circuit board further comprises a first conductive layer disposed over the first surface of the dielectric layer, and after forming the first blind hole in the dielectric layer, the first blind hole is passed through the first conductive layer.

**[Claim 4]** 4. The method of forming conductive column of claim 3, wherein the circuit board further comprises a second conductive layer disposed over the second surface of the dielectric layer, and after forming the second blind hole in the dielectric layer, the second blind hole is passed through the second conductive layer.

**[Claim 5]** 5. The method of forming conductive column of claim 1, wherein the first blind hole is formed by a mechanical drilling method.

**[Claim 6]** 6. The method of forming conductive column of claim 5, wherein the second blind hole is formed by a mechanical drilling method.

**[Claim 7]** 7. The method of forming conductive column of claim 1, wherein the first blind hole is formed by a laser drilling method.

[Claim 8] 8. The method of forming conductive column of claim 7, wherein the second blind hole is formed by a laser drilling method.

[Claim 9] 9. The method of forming conductive column of claim 1, wherein the step of filling the conductive material comprises plating process.

[Claim 10] 10. The method of forming conductive column of claim 9, wherein the conductive material fills the through hole from a position where the blind ends of the first and second blind holes connect with each other in the through hole and extends towards the first surface and or the second surface.

[Claim 11] 11. A circuit board, comprising:

a dielectric layer, having a first surface, a second surface opposite to the first surface and at least one through hole, wherein the through hole passes through the dielectric layer, and an inner diameter of the through hole near the first or the second surface is substantially larger than an inner diameter of the through hole near a middle portion of the through hole; and  
at least one conductive column, disposed in the through hole, a shape of the conductive column being substantially consistent to a shape of the through hole.

[Claim 12] 12. A circuit board of claim 11, wherein the through hole and the conductive column have an hourglass shape.

[Claim 13] 13. A circuit board, comprising:

a dielectric layer, having a first surface and a second surface opposite to the first surface; and  
a first conductive layer, disposed over the first surface of the dielectric layer, wherein a through hole is formed in the first dielectric layer and the first conductive layer passing through the dielectric layer and the first conductive layer, and an inner diameter of the through hole near the first or the second surface is substantially larger than an inner diameter of the through hole near a middle portion of the through hole; and

at least one conductive column, disposed in the through hole, a shape of the conductive column being substantially consistent to a shape of the through hole.

[Claim 14] 14. A circuit board of claim 13, wherein the through hole and the conductive column have an hourglass shape.

[Claim 15] 15. A circuit board of claim 13, further comprising a second conductive layer disposed over the second surface of the dielectric layer, and wherein the through hole passes through the second conductive layer.